

## **ABSTRACT**

A biasing circuit and a voltage control oscillator thereof are provided. The biasing circuit comprises a compensation circuit, a delay circuit and a comparison circuit. In the biasing circuit, the compensation circuit compensates a first differential voltage that is generated by the delay circuit so as to stabilize an output of the biasing circuit operated by a low-current or low-frequency. The jitter of the clock frequency that the voltage control oscillator output can be reduced.